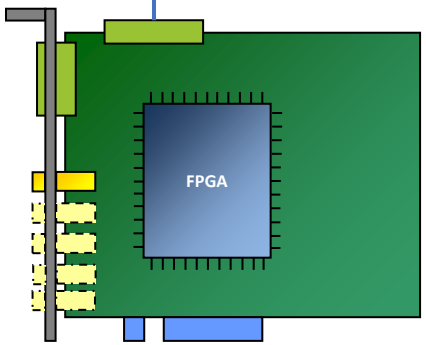
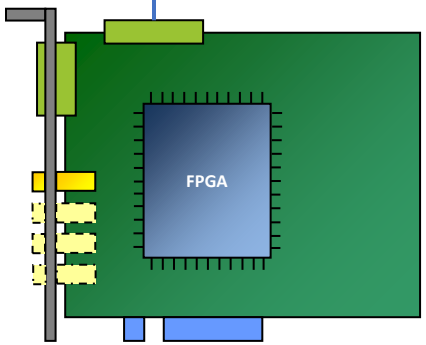
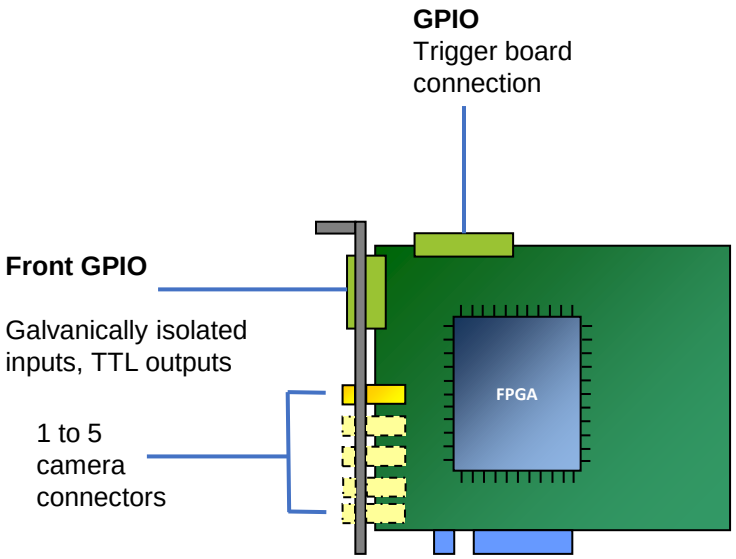
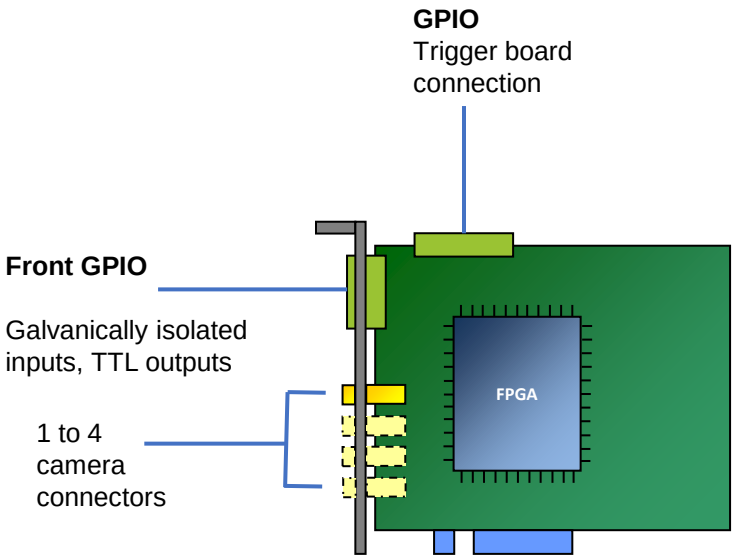
GPIO
Trigger board
connection



GPIO
Trigger board
connection

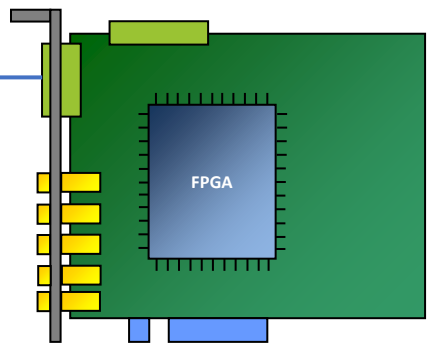



GPIO
Trigger board
connection
Front GPIO
Galvanically isolated
inputs, TTL outputs
1 to 5
camera
connectors
FPGA


GPIO
Trigger board
connection
Front GPIO
Galvanically isolated
inputs, TTL outputs
1 to 4
camera
connectors
FPGA

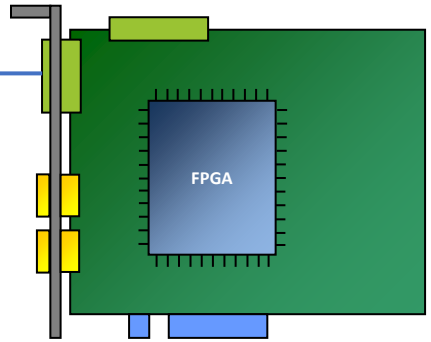
Front GPIO

Galvanically isolated
inputs, TTL outputs

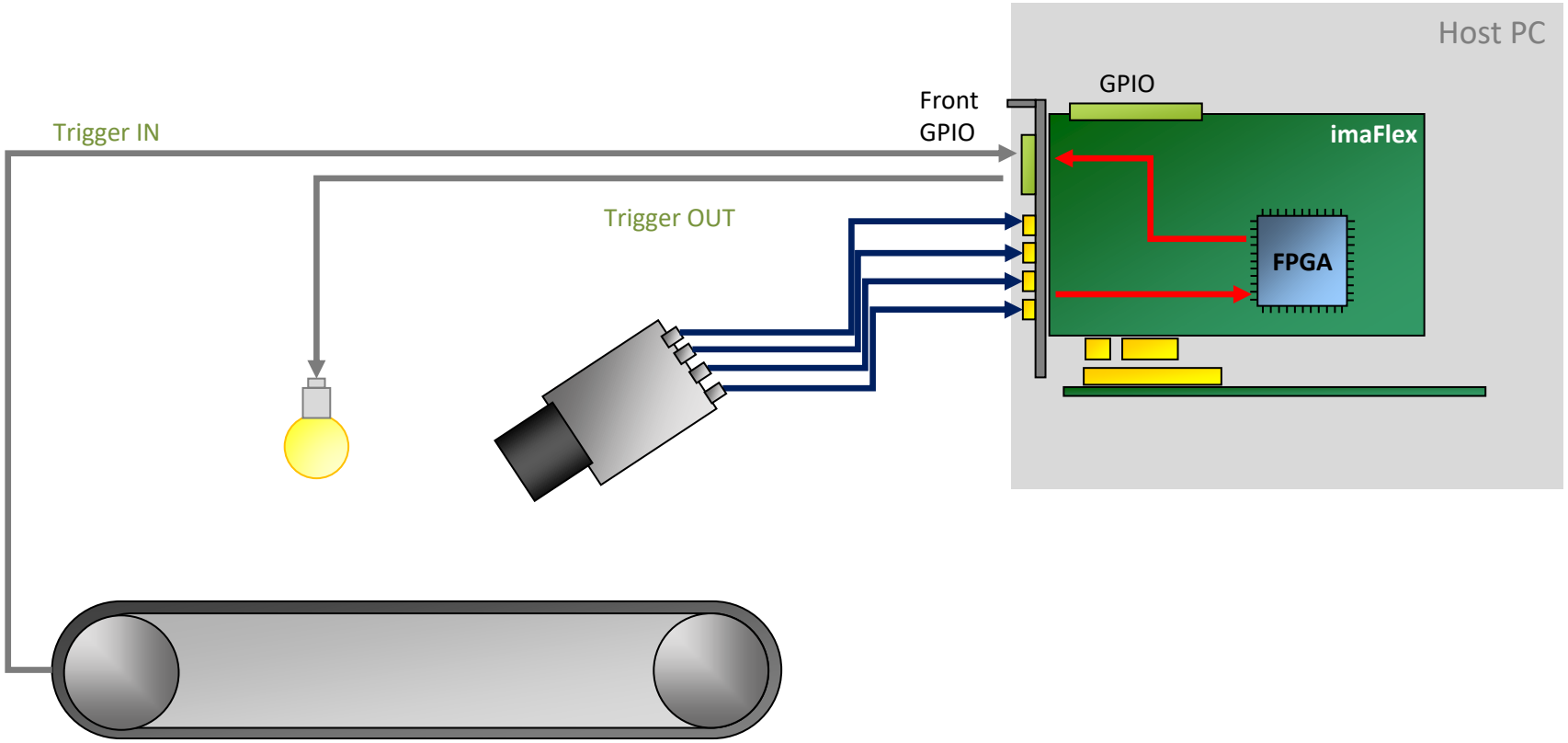


Front GPIO

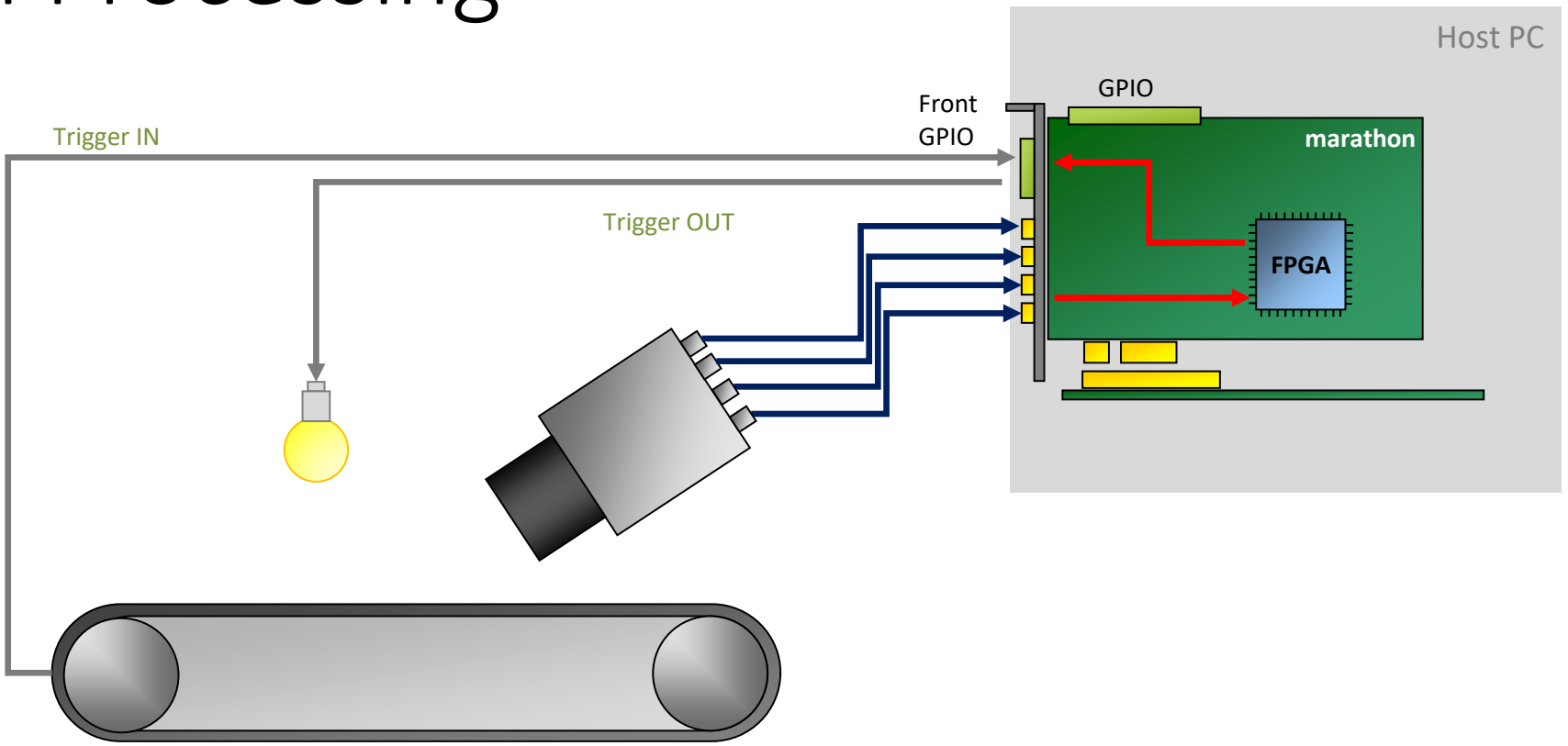
Galvanically isolated
inputs, TTL outputs



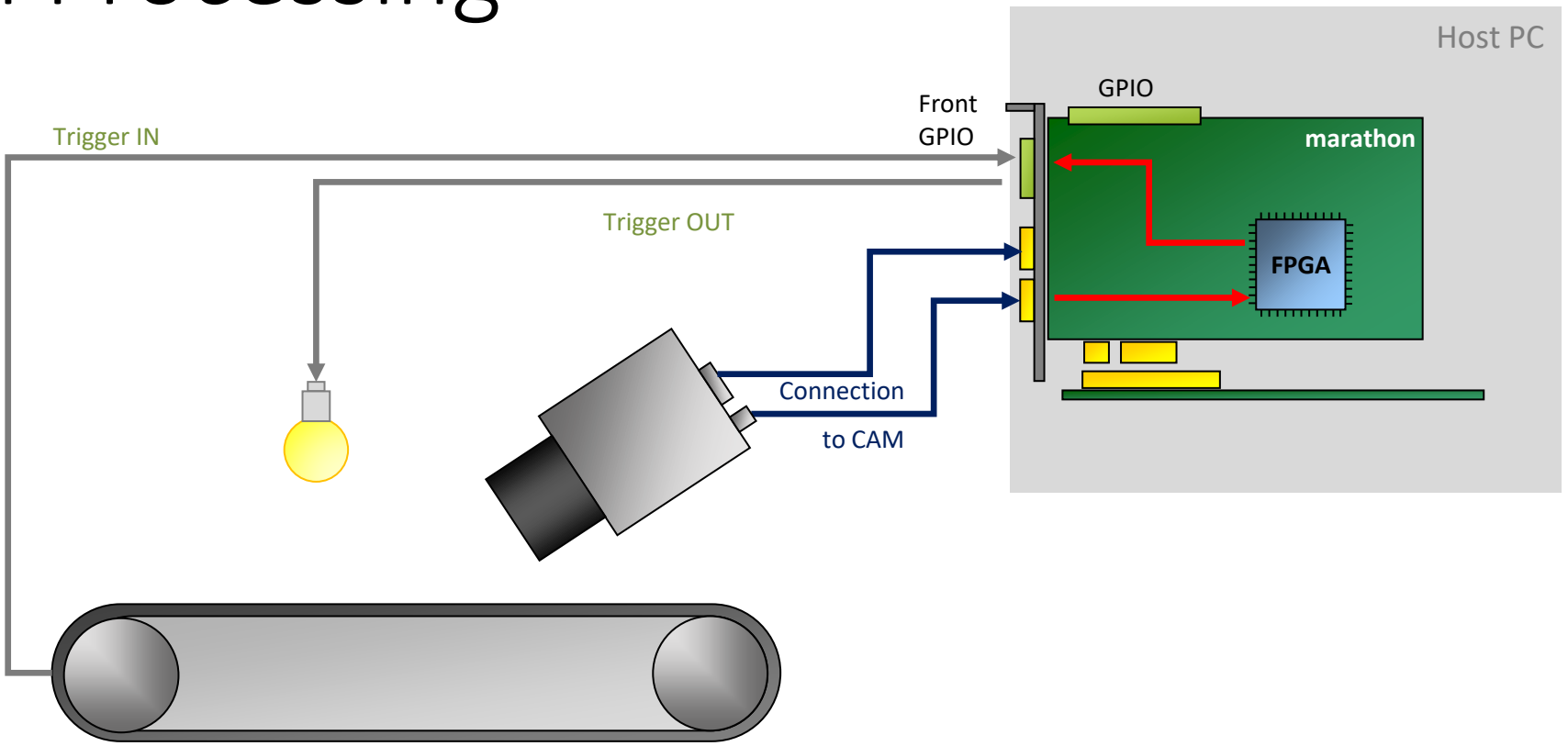
Signal Processing imaFlex



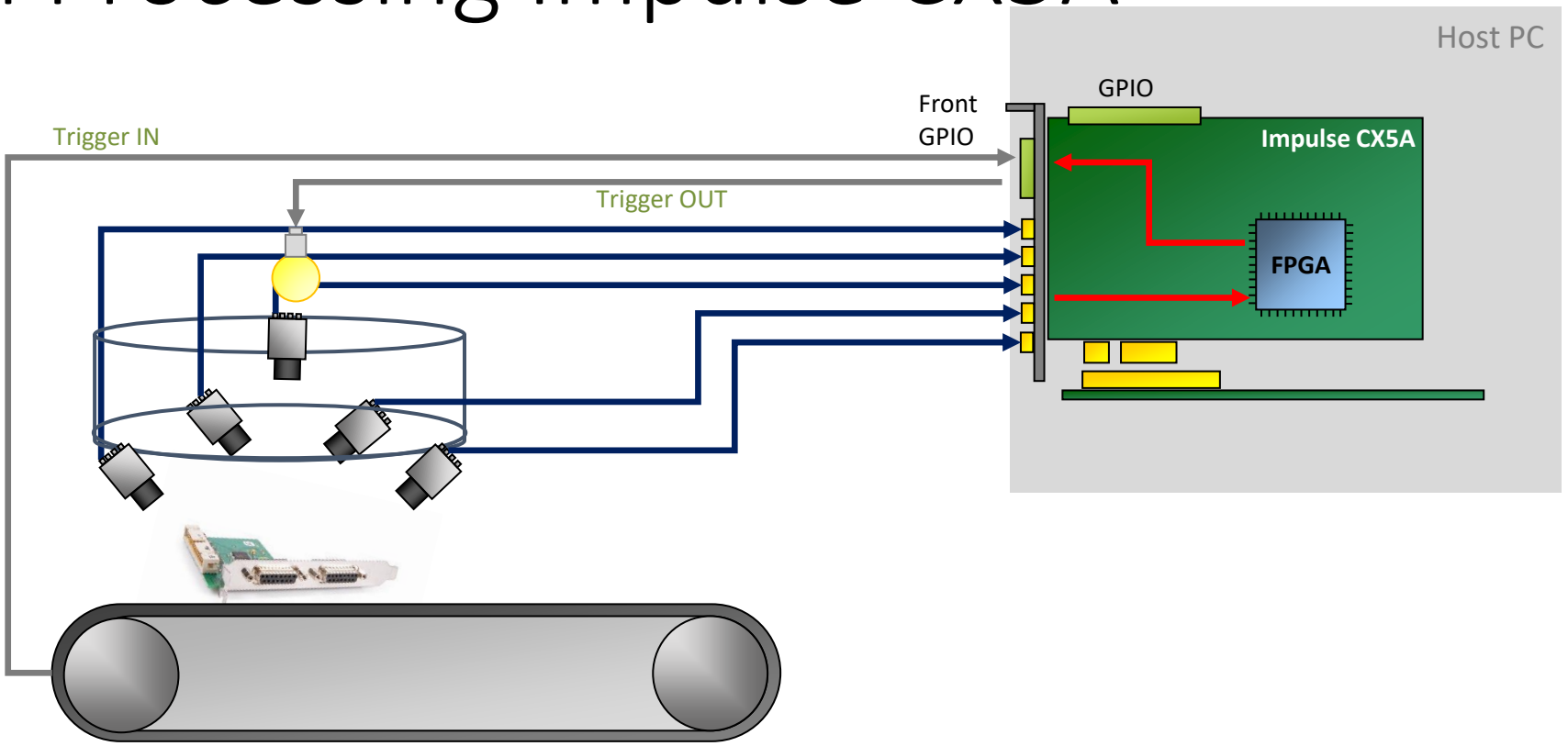
Signal Processing



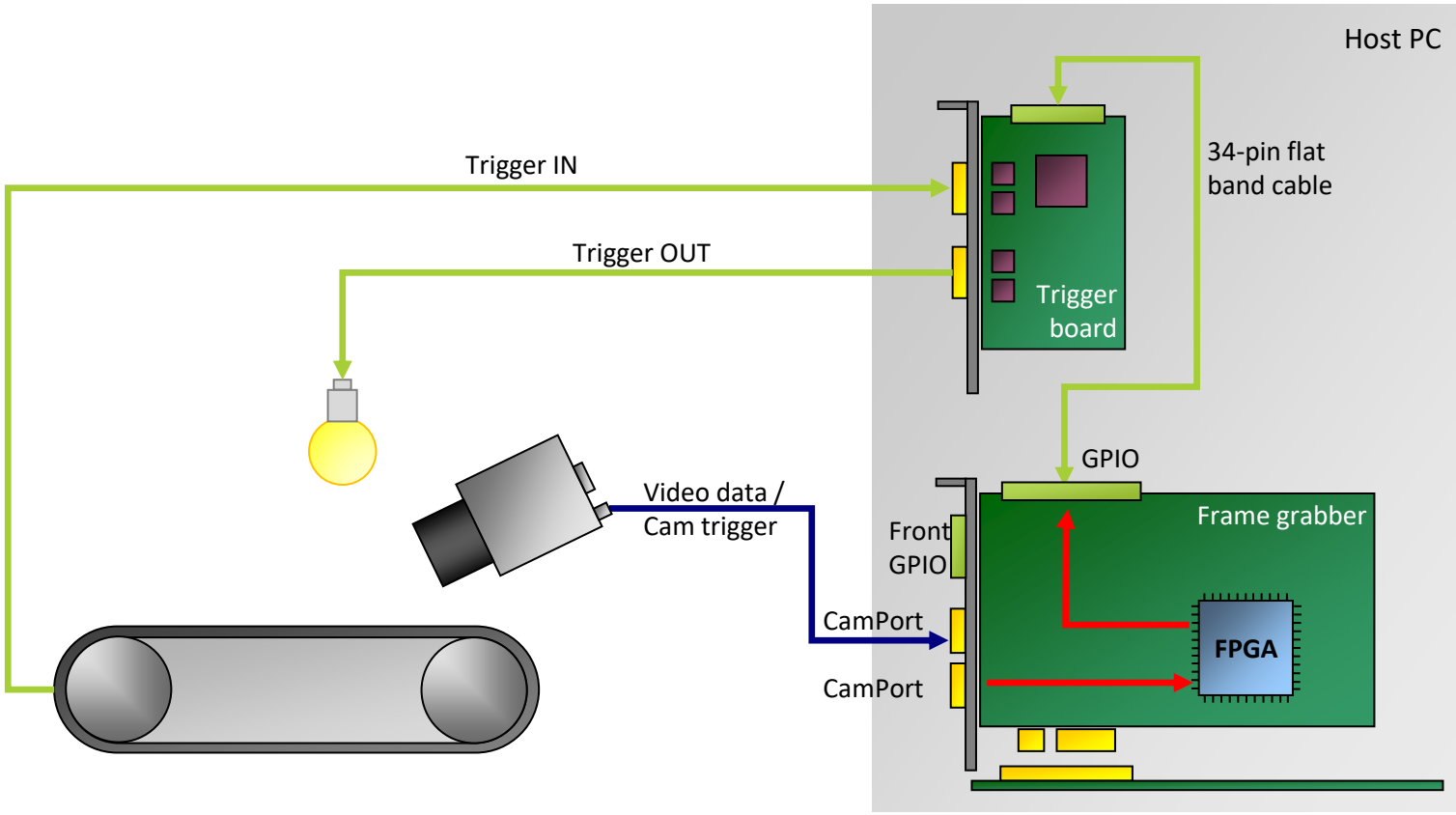
Signal Processing



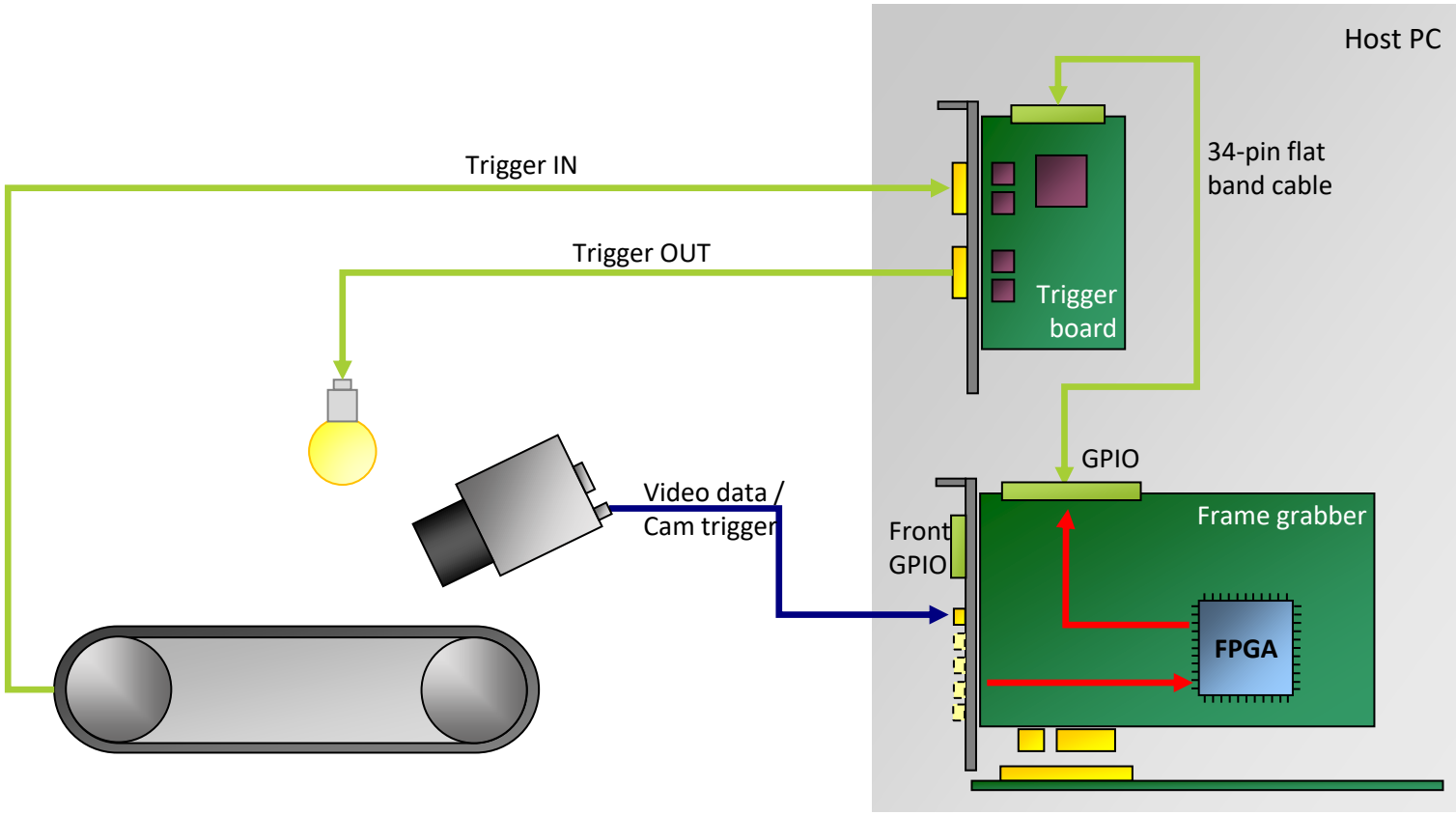
Signal Processing Impulse CX5A



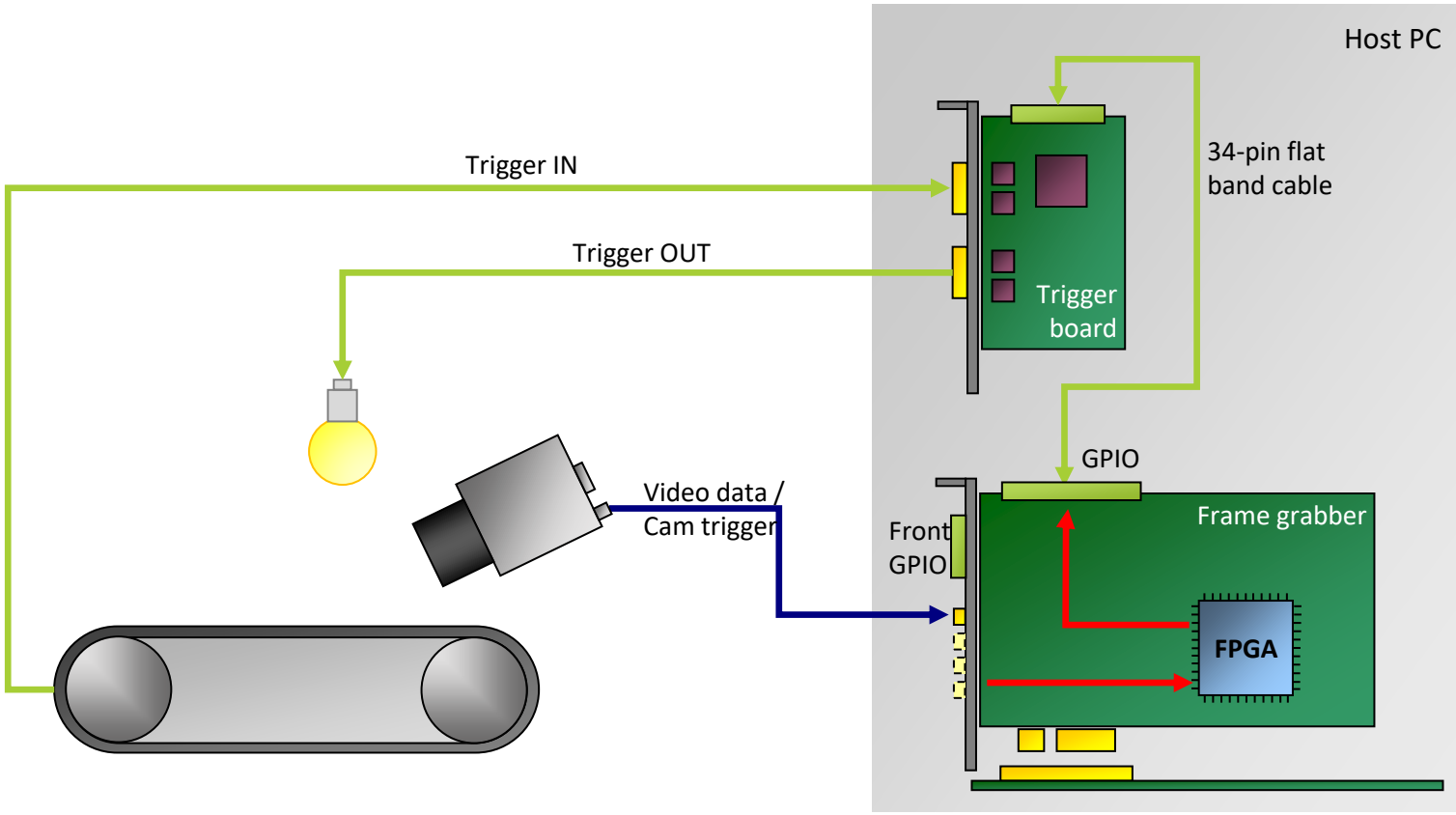
Trigger GPIO Out



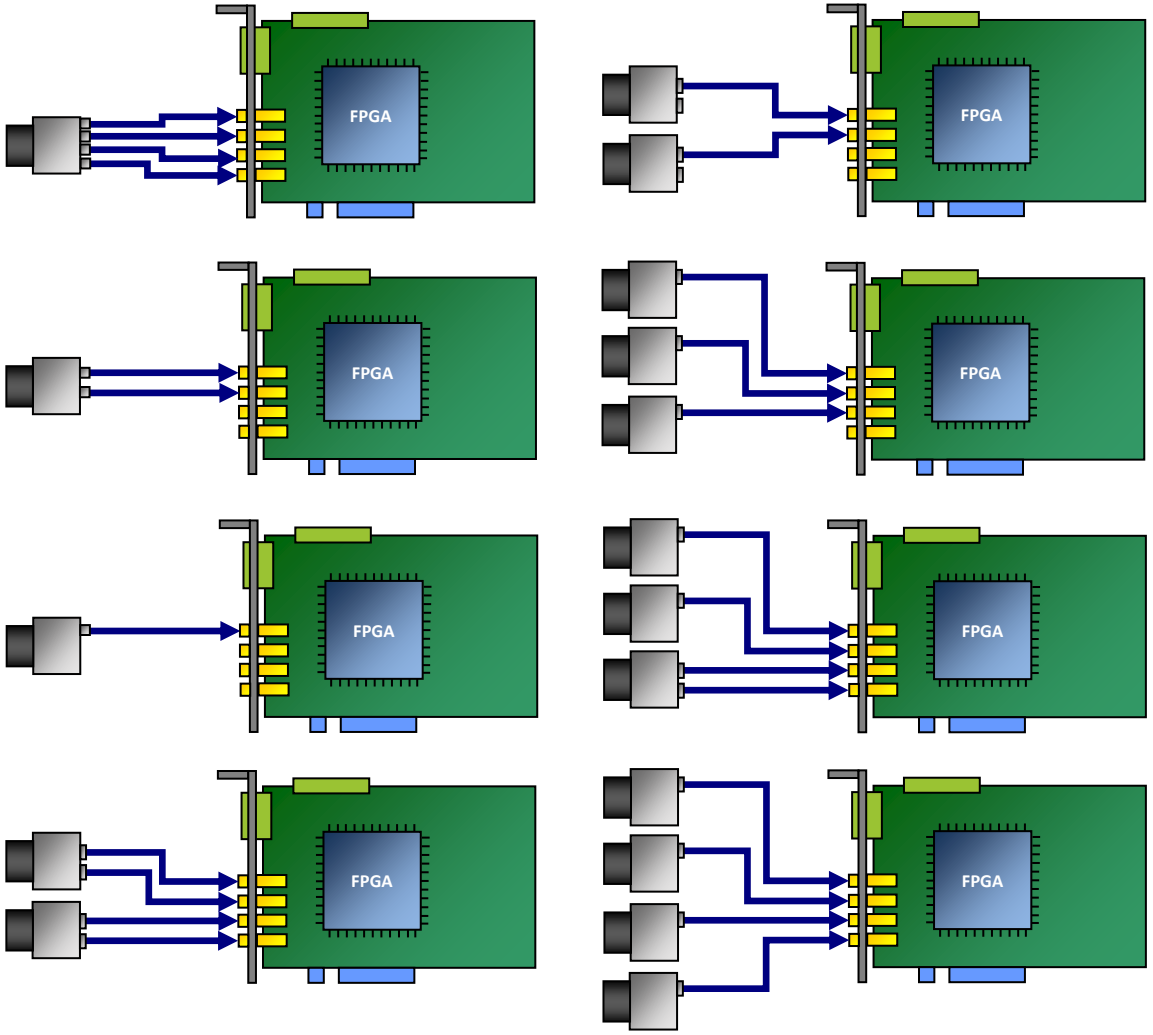
Trigger GPIO Out

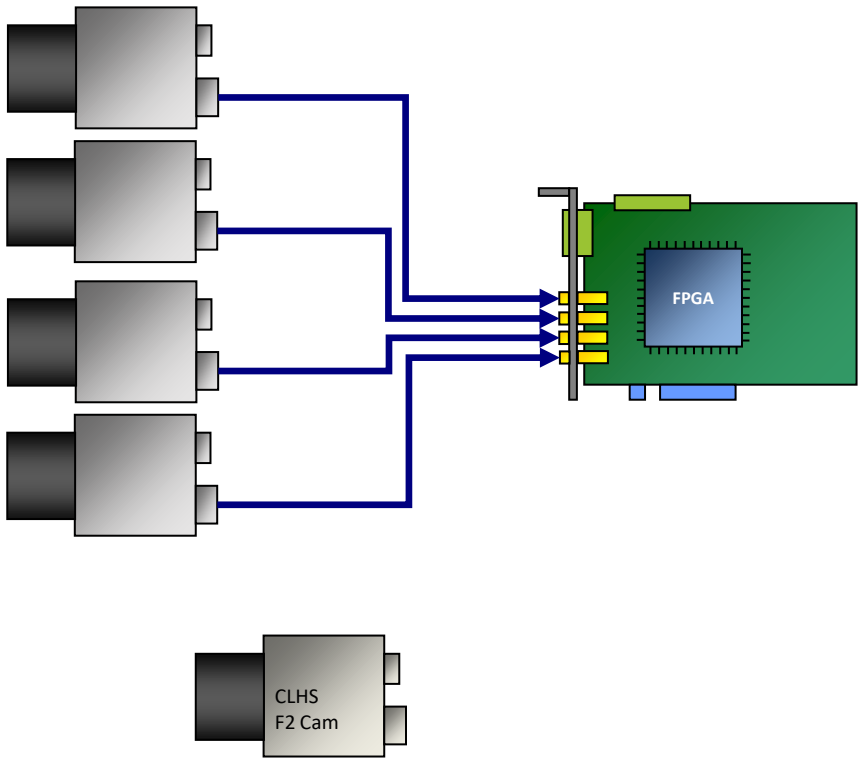


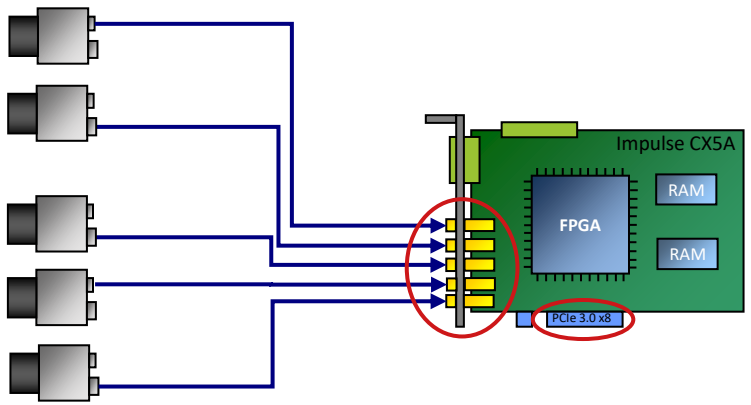
Trigger GPIO Out

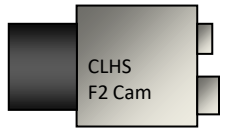


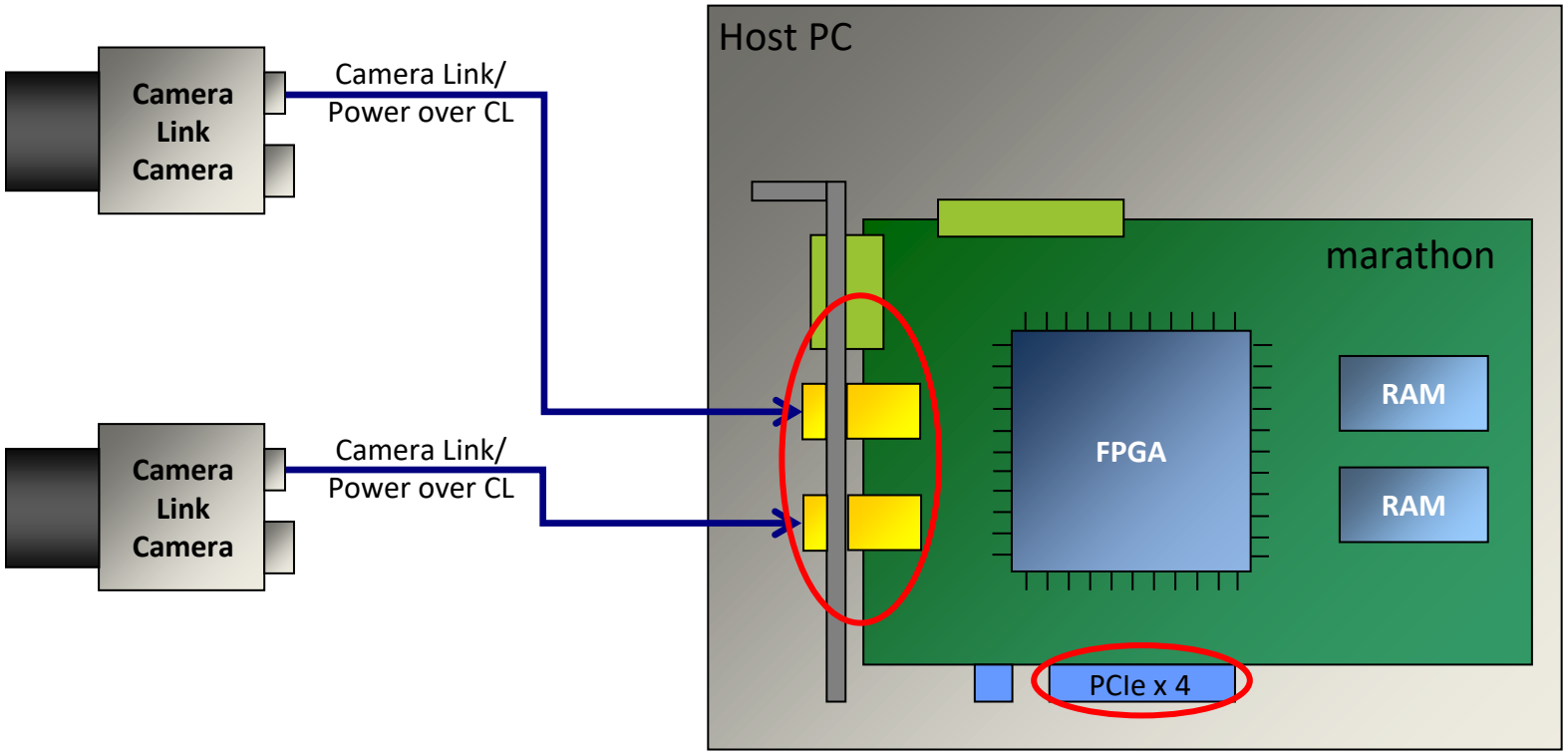
imaWorx Topology

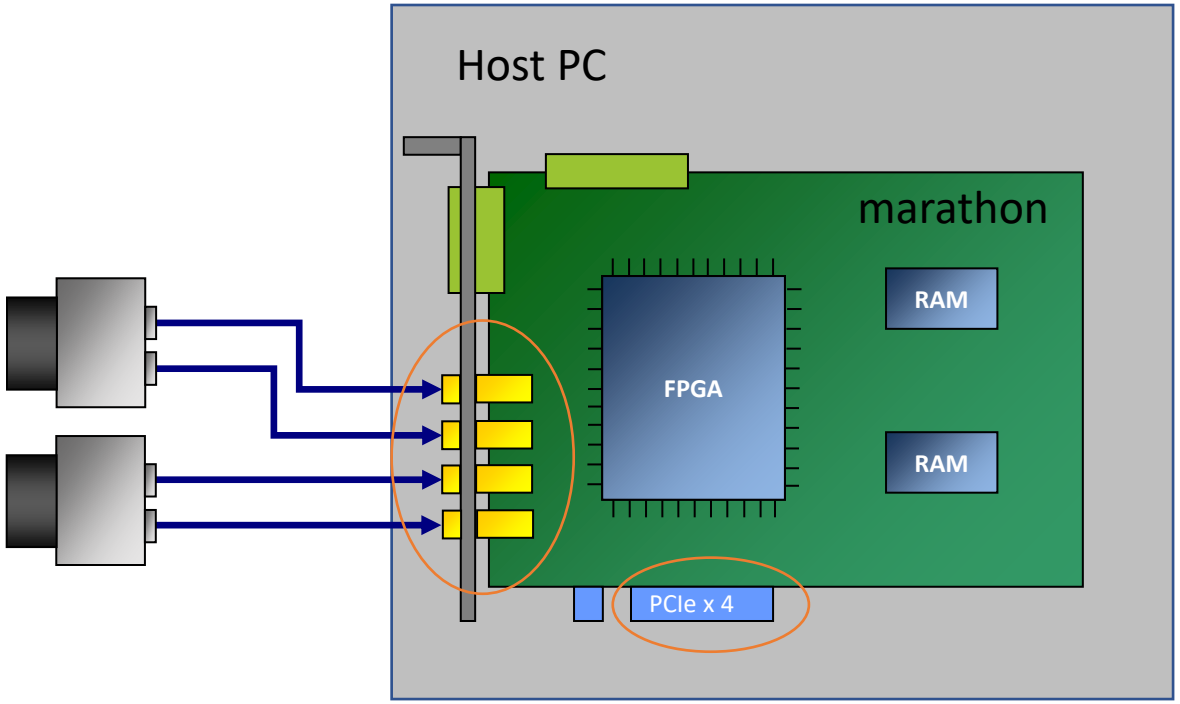


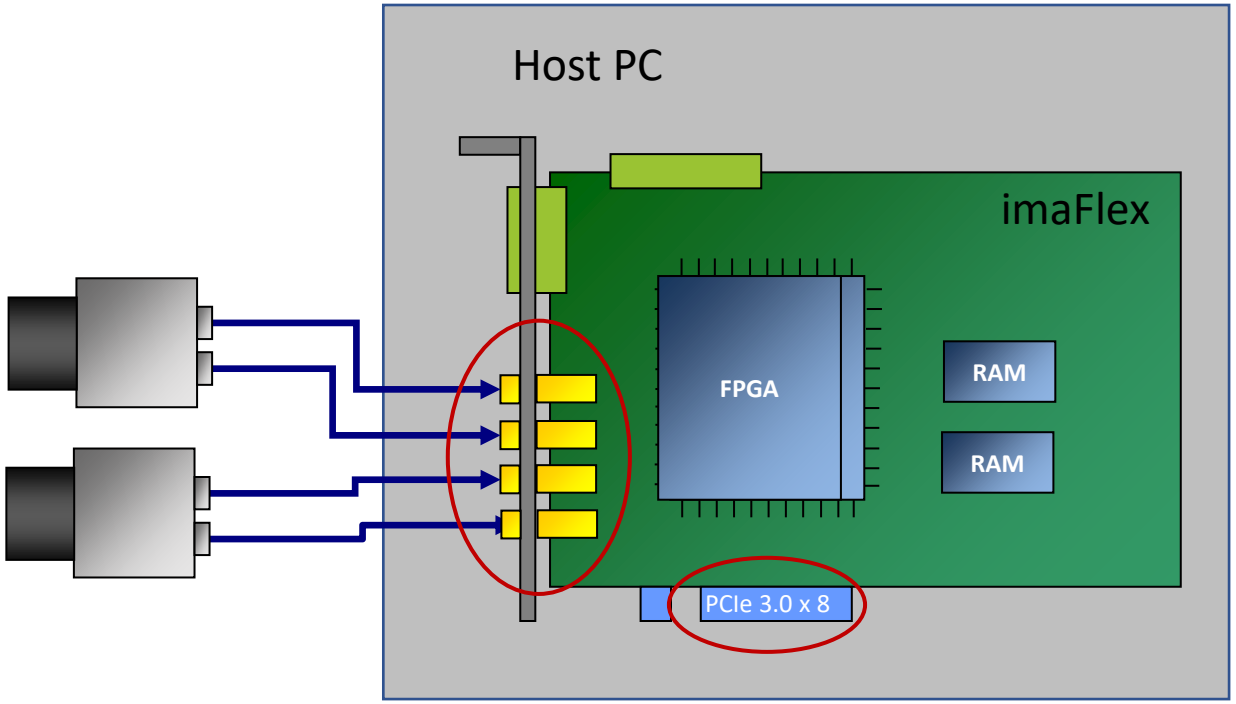

FPGA
CLHS
F2 Cam

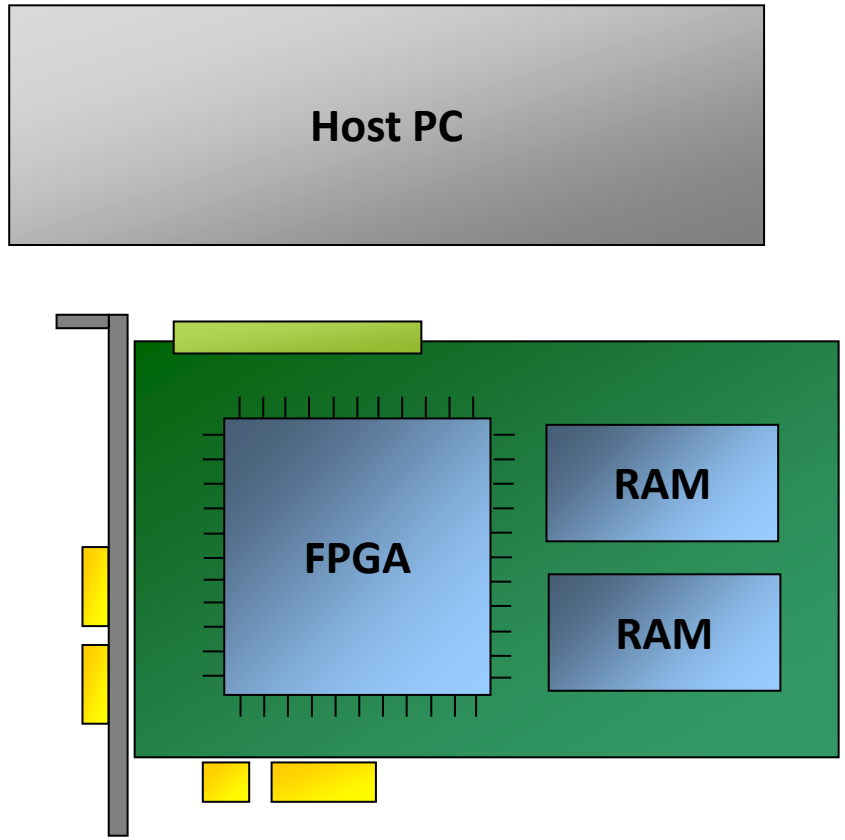

Impulse CX5A
FPGA
RAM
RAM
PCIe 3.0 x8


CLHS
F2 Cam

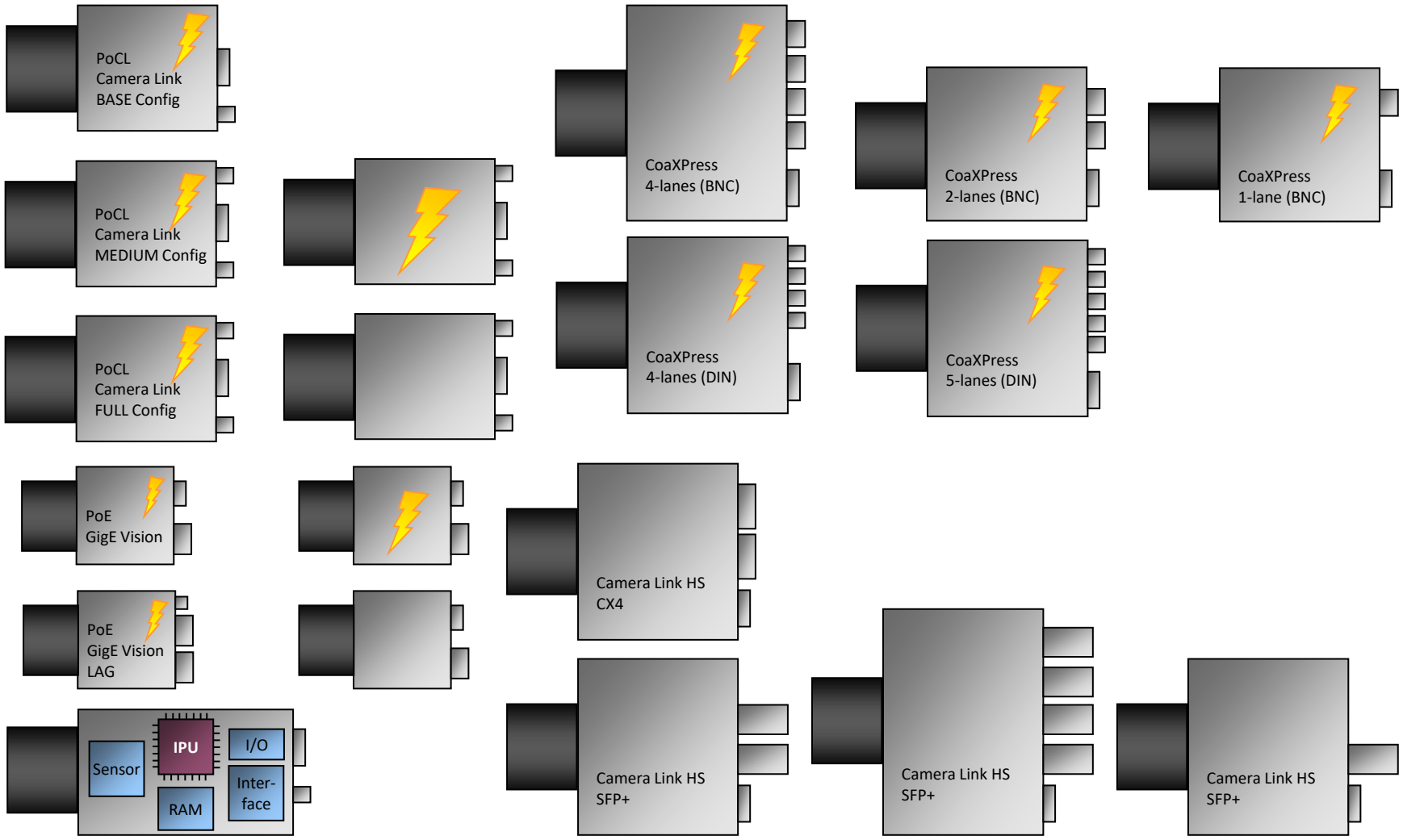

Camera
Link
Camera
Camera Link/
Power over CL
Camera
Link
Camera
Camera Link/
Power over CL
Host PC
marathon
FPGA
RAM
RAM
PCIe x 4


Host PC
marathon
FPGA
RAM
RAM
PCIe x 4


Host PC
imaFlex
FPGA
RAM
RAM
PCIe 3.0 x 8


Host PC
FPGA
RAM
RAM

Cameras


PoCL
Camera Link
BASE Config
PoCL
Camera Link
MEDIUM Config
PoCL
Camera Link
FULL Config
PoE
GigE Vision
PoE
GigE Vision
LAG
Sensor
IPU
I/O
Inter-
face
RAM
CoaXPress
4-lanes (BNC)
CoaXPress
4-lanes (DIN)
CoaXPress
2-lanes (BNC)
CoaXPress
5-lanes (DIN)
CoaXPress
1-lane (BNC)
Camera Link HS
CX4
Camera Link HS
SFP+
Camera Link HS
SFP+
Camera Link HS
SFP+

Applications

